


AMENDMENTS TO THE SPECIFICATION:

Please replace the title of the invention with the following:

A Memory System for Electronic Equipments and the Method Thereof

Please amend the second paragraph on page 7 as follows:

The access control to each of the memory modules 13, 14, and 15 from the memory controller 121 can be performed through the memory bus 4. The memory bus 4 in the computer system 10 is designated ~~so as to~~ operate at an established operating frequency, for example, of 100 MHz, by a frequency controller 122 included in memory controller 121. Of course, it is possible to change the designated operating frequency for the memory bus 4. In order to change the designated operating frequency for the memory bus 4, the memory controller 121 selects a desired clock signal that is generated from a clock generator (not shown) for operating the memory bus 4.

FINNEGAN
HENDERSON
FARABOW
GARRETT &
DUNNER LLP

1300 I Street, NW
Washington, DC 20005
202.408.4000
Fax 202.408.4400
www.finnegan.com